Schedule

Day 1:	**Saturday, 16 August**
15:30	**Registration** **(Voltaire, 5F)**
18:30	**Welcome Reception Dinner** **(Rousseau + Descartes + Voltaire + Pascal, 5F)**
Day 2:	**Sunday, 17 August**
08:30	**Registration** **(Grand Hall A, 5F)**
09:00	**Opening** **(Grand Hall A, 5F)**
09:00	Welcome Message by General Chair: Prof. Chen-Fu Chien, National Tsing Hua University, Taiwan
09:10	Keynote Speech Data-based Scheduling of Semiconductor Manufacturing Fabrication Facility *Professor Mengchu Zhou, New Jersey Institute of Technology, USA* Chair: Professor Jei-Zheng Wu, Soochow University, Taiwan
09:50	Break
10:10	Keynote Speech II Models for the Throughput of Clustered Photolithography Tools with Applications *Professor James R. Morrison, KAIST, South Korea* Chair: Dr. C. Hsu, Taiwan Semiconductor Manufacturing Company
10:50	Break
11:00	Student Paper Award Finalist Chair: Professor Chia-Yen Lee, National Cheng Kung University, Taiwan
12:00	**Lunch** **(Rousseau + Descartes, 5F)**
13:00	**Technical Sessions**
13:00	Tutorial Talk (Grand Hall A, 5F) (I) Post FAB Complexity – Litho is not the End of the Known World *Dr. Kenneth Fordyce (IBM retired), Director of Analytics, Arkieva, USA* (II) Interpolation Approximations for Queues in Series *Professor Kan Wu, Nanyang Technological University, Singapore* (III) Simulation-based Performance Assessment of Production Planning and Scheduling Approaches in Complex Manufacturing Systems *Professor Lars Mönch, University of Hagen, Germany* Parallel Session A: Scheduling & Dispatching (Pascal, 5F) Parallel Session B: Modeling & Decisions (Voltaire, 5F)
14:40	Break
14:50	Parallel Session C: Manufacturing Intelligence (Grand Hall A, 5F) Parallel Session D: Manufacturing Excellence (Pascal, 5F) Parallel Session E: Quality Engineering (Voltaire, 5F)
16:30	**Break**
18:30	**Banquet & Award Ceremony** **(Rousseau + Descartes + Voltaire + Pascal, 5F)**
Day 3:	**Monday, 18 August**
09:30	**Industrial Visit: Delta Electronics, Inc.**
11:30	**Transportation to Fullon Hotel at Tamsui Fishermen's Wharf (IEEE CASE2014)**